1 Introduction
Decoder interface standards exist in order to aid in installation of decoders into vehicle system boards. Installation can be performed by the vehicle manufacturer or separately by the individual modeler.

2 Served Use Cases
The intent is that any decoder which conforms to the standard, from any manufacturer, will operate with any vehicle system board which conforms to the standard, from any manufacturer. Operate is further defined as not to cause any permanent damage of the decoder or vehicle system board.

2.1 Unserved Use Cases
It is not the purpose of the standard to ensure that every decoder and vehicle system board implements all of the possible features defined by the standard. Operation is limited to the features supported by the decoder and/or vehicle system board, as documented by the respective manufacturers.

3 Annotations to the Standard

3.1 References
Additional relevant references are found in S-9.1.1.3.

- S-9.1.1.3 21MTC Decoder Interface

3.2 Requirements
It is important to note that a product is not required to implement all connections of the interface. A manufacturer may choose to omit certain features, for example the number of function outputs. It is incumbent on the manufacturer to document for users the supported features of their product(s).

3.3 Mechanical Properties
The mechanical properties are provided in general terms so as not to codify into the standard any limit to alternative sources. Proper connector sources will result in the proper seating of the decoder on the locomotive mainboard so that the decoder’s socket mounts flush with the mainboard pin header. TI-9.1.1 contains a list of manufacturer and part numbers for connector sources that have been tested for conformance to the standard.

The NMRA Conformance and Inspection committee may allow exemptions up to 3.5 mm maximum in mainboard pin header height, at their discretion, depending on when the decoder under test was originally designed.
3.4 Socket Specifications

3.4.1 Pin Header Specifications
If a male pin header is provided that is terminated in wires, it is recommended that the wire colors follow the color codes defined in S-9.1.1.

3.5 Electrical Specifications

3.5.1 Function Output Variations
The 21MTC defines output pins 7, 8, 14, and 15 as Open Collector/Drain switched (heretofore indicated as OC) outputs. Variations exist with output pins 3, 4, 13, and 17, identified as outputs AUX3 to AUX6. Refer to Table 1 below. S-9.1.1.3 requires providing outputs higher than AUX2 as logic level outputs. While AUX3 through AUX6 are always required to support TTL/LVTTL logic-level outputs, an open collector/drain option switched against ground may be provided. The default configuration must support TTL/LVTTL logic level on AUX3 through AUX6.

<table>
<thead>
<tr>
<th>Pin</th>
<th>Output</th>
<th>Open Collector</th>
<th>Logic Level</th>
<th>Notes</th>
</tr>
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<tbody>
<tr>
<td>1</td>
<td>AUX7</td>
<td>●</td>
<td>●</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>AUX8</td>
<td>●</td>
<td>●</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>AUX6</td>
<td>●</td>
<td>●</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>AUX4</td>
<td>●</td>
<td>●</td>
<td>Supported as LL on OEM motherboards</td>
</tr>
<tr>
<td>5</td>
<td>AUX9</td>
<td>●</td>
<td>●</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>AUX10</td>
<td>●</td>
<td>●</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>F0r</td>
<td>●</td>
<td>●</td>
<td>Standard Reverse Light Output</td>
</tr>
<tr>
<td>8</td>
<td>F0f</td>
<td>●</td>
<td>●</td>
<td>Standard Forward Light Output</td>
</tr>
<tr>
<td>13</td>
<td>AUX3</td>
<td>●</td>
<td>●</td>
<td>Supported as LL on OEM motherboards</td>
</tr>
<tr>
<td>14</td>
<td>AUX2</td>
<td>●</td>
<td>●</td>
<td>Supported as OC on OEM motherboards</td>
</tr>
<tr>
<td>15</td>
<td>AUX1</td>
<td>●</td>
<td>●</td>
<td>Supported as OC on OEM motherboards</td>
</tr>
<tr>
<td>17</td>
<td>AUX5</td>
<td>●</td>
<td>●</td>
<td></td>
</tr>
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</table>

Table 1: Function Output Variations
3.6 Existing variant

There is a variant of the 21MTC interface on the market that was created before the standardization by the MOROP and does not correspond to this standard. This variant is documented here in order to show the differences.

According to this standard, only the outputs F0f (Pin 8), F0r (Pin 7), AUX1 (Pin 15) and AUX2 (Pin 16) are amplified outputs, i.e. equipped with a driver transistor switching to GND on the decoder. In the variant, the outputs AUX3 and AUX4 (Pins 13 and 4) are also implemented as amplified outputs and not as outputs with logic level. The addition is AUX6 (Pin 3) is used as input.

This variant is mainly used by Märklin. This variant is used in all newer vehicles from Märklin, but not in older vehicles. Therefore, when replacing the factory-installed decoder, you should check which type of decoder is required. There is a corresponding list on the Märklin company homepage.

Products that have an interface corresponding to this variant must be marked with "21MTC-M" and the instructions must refer to the amplified outputs. Under this condition, after the usual test, the products according to this variant can also bear the NMRA conformity logo.

Products with or for amplified outputs AUX5 and AUX6 (Pins 17 and 3) do not comply with this standard and may not bear the NMRA conformity logo.

4 Document History

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<tr>
<th>Date</th>
<th>Description</th>
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<tr>
<td>Dec 1, 2020</td>
<td>First Release of TN-9.1.1.3</td>
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